

WHAT IS CLAIMED IS:

1 1. A bus interface unit for transferring data between a
2 plurality of bus devices, said bus interface unit comprising:

3 a first bus device interface comprising:

4 a first incoming request bus for receiving request
5 packets from a first one of said plurality of bus devices;

6 a first outgoing request bus for transmitting
7 request packets to said first bus device;

8 a first incoming data bus for receiving data packets
9 from said first bus device; and

10 a first outgoing data bus for transmitting data
11 packets to said first bus device; and

12 a second bus device interface comprising:

13 a second incoming request bus for receiving request
14 packets from a second one of said plurality of bus devices;

15 a second outgoing request bus for transmitting
16 request packets to said second bus device;

17 a second incoming data bus for receiving data
18 packets from said second bus device; and

19 a second outgoing data bus for transmitting data
20 packets to said second bus device.

1 2. The bus interface unit as set forth in Claim 1 wherein a
2 first one of said request packets received on said first incoming
3 request bus comprises a physical address field and a request type
4 field.

1 3. The bus interface unit as set forth in Claim 2 wherein
2 said first request packet further comprises a priority field.

1 4. The bus interface unit as set forth in Claim 3 wherein
2 said request type field comprises a write data indicator indicating
3 that said first request packet is a first write data request
4 operable to transfer a first data block stored in said first bus
5 device to said second bus device.

1 5. The bus interface unit as set forth in Claim 4 wherein a
2 first one of said data packets received on said first incoming data
3 bus is associated with said first write data request.

1 6. The bus interface unit as set forth in Claim 3 wherein
2 said request type field comprises a read data indicator indicating
3 that said first request packet is a first read data request
4 operable to transfer a second data block stored in said second bus
5 device to said first bus device.

1 7. The bus interface unit as set forth in Claim 1 wherein a
2 first one of said request packets received on said first incoming
3 request bus comprises a source identification value identifying an
4 initiating bus device that initiated said first request packet.

1 8. The bus interface unit as set forth in Claim 7 wherein
2 said first request packet comprises a destination identification
3 value identifying a recipient bus device to which said first
4 request packet is being transmitted.

1 9. An integrated circuit data comprising:

2 N bus devices capable of transferring data with one
3 another; and

4 a bus interface unit for transferring data between said N
5 bus devices, said bus interface unit comprising N bus interfaces,
6 each of said N bus interfaces comprising:

7 an incoming request bus for receiving request
8 packets from a first one of said plurality of bus devices;

9 an outgoing request bus for transmitting request
10 packets to said first bus device;

11 an incoming data bus for receiving data packets from
12 said first bus device; and

13 an outgoing data bus for transmitting data packets
14 to said first bus device.

1 10. The integrated circuit as set forth in Claim 9 wherein a
2 first one of said request packets received on said first incoming
3 request bus comprises a physical address field and a request type
4 field.

1 11. The integrated circuit as set forth in Claim 10 wherein
2 said first request packet further comprises a priority field.

1 12. The integrated circuit as set forth in Claim 11 wherein
2 said request type field comprises a write data indicator indicating
3 that said first request packet is a first write data request
4 operable to transfer a first data block stored in said first bus
5 device to a second one of said plurality of bus devices.

1 13. The integrated circuit as set forth in Claim 12 wherein a
2 first one of said data packets received on said first incoming data
3 bus is associated with said first write data request.
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1 14. The integrated circuit as set forth in Claim 11 wherein
2 said request type field comprises a read data indicator indicating
3 that said first request packet is a first read data request
4 operable to transfer a second data block stored in a second one of
5 said plurality of bus devices to said first bus device.

1 15. The integrated circuit as set forth in Claim 9 wherein a
2 first one of said request packets received on said first incoming
3 request bus comprises a source identification value identifying an
4 initiating bus device that initiated said first request packet.

1 16. The integrated circuit as set forth in Claim 15 wherein
2 said first request packet comprises a destination identification
3 value identifying a recipient bus device to which said first
4 request packet is being transmitted.

1 17. For use in a bus interface unit comprising N bus
2 interfaces, each of the N bus interfaces comprising: i) an incoming
3 request bus for receiving request packets; ii) an outgoing request
4 bus for transmitting request packets; iii) an incoming data bus for
5 receiving data packets; and iv) an outgoing data bus for
6 transmitting data packets, a method of transferring data to a first
7 bus device from a second bus device, the method comprising the
steps of:

 receiving a data read request packet from the first bus
device on an incoming request bus coupled to the first bus device;

 transmitting the data read request packet to the second
bus device on an outgoing request bus coupled to the second bus
device;

 receiving a data packet from the second bus device on an
incoming data bus coupled to the second bus device; and

 transmitting the data packet to the first bus device on
an outgoing data bus coupled to the first bus device.

18. The method as set forth in Claim 17 further comprising the step of receiving an acknowledgment response packet from the second device on an incoming request bus coupled to the second bus device concurrently with the step of receiving the data packet from the second bus device.

19. For use in a bus interface unit comprising N bus interfaces, each of the N bus interfaces comprising: i) an incoming request bus for receiving request packets; ii) an outgoing request bus for transmitting request packets; iii) an incoming data bus for receiving data packets; and iv) an outgoing data bus for transmitting data packets, a method of transferring data from a first bus device to a second bus device, the method comprising the steps of:

receiving a data write request packet from the first bus device on an incoming request bus coupled to the first bus device;

receiving a data packet from the first bus device on an incoming data bus coupled to the first bus device;

transmitting the data write request packet to the second bus device on an outgoing request bus coupled to the second bus device; and

transmitting the data packet to the second bus device on an outgoing data bus coupled to the second bus device.

1 20. The method as set forth in Claim 19 wherein the step of
2 receiving the data write request packet and the step of receiving
3 the data packet are concurrent.

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